Figure 3. Detailed Noise Test Circuitry. Thermally Lagged Q1-Q2 Low Noise J-FET Pair Is DC Stabilized by A1-Q3; A2 Delivers A = 10,000 Pre-Amplifier Output. A3-A4 form 0.1Hz to 10Hz, A = 100, Bandpass Filter; Total Gain Referred to Pre-Amplifier Input Is 10^5. Peak to Peak Noise Detector, Reset by Monitoring Oscilloscope Sweep Gate, Supplies DVM Output.
DUT
GAIN = 1000

0.1Hz LPF
2nd order
G = 10

10Hz HPF
2nd order
G = 10

10Hz HPF
2nd order
G = 1
c4757p – Jan 2014 – Op amp for amplifying 5uV of noise to 200uV
Gerhard Hoffmann – Sep 2014 – A 220 pV/sqrt(Hz) low noise preamplifier
10.014 times

paddle may float

12.4 Ohm is marked "10R" on the chips (braindead!)
Measuring 2nV/√Hz Noise and 120dB Supply Rejection on Linear Regulators (single input)
Measuring 2nV/√Hz Noise and 120dB Supply Rejection on Linear Regulators (differential input)